1. Verify the truth table of NAND gate with necessary logic diagram.
2. Verify the truth table of SR flip-flop with necessary logic diagram.
3. Verify the truth table of NOR gate with necessary logic diagram.
4. Verify the truth table of D flip-flop with necessary logic diagram.
5. Verify the truth table of XOR gate with necessary logic diagram.
6. Verify the truth table of SR latch with necessary logic diagram.
7. Verify the truth table of XNOR gate with necessary logic diagram.
8. Verify the truth table of D latch with necessary logic diagram.
9. Verify the truth table of AND gate with necessary logic diagram.
10. Design the half-adder circuit and verify the truth table.
11. Verify the truth table of OR gate with necessary logic diagram.
12. Design half subtractor circuit and verify the truth table.
13. Verify the truth table of NOT gate with necessary logic diagram.
14. Design the full adder circuit with necessary logic diagram.
15. Design and verify 2-to-4 decoder.
16. Design and verify 4x1 multiplexer.